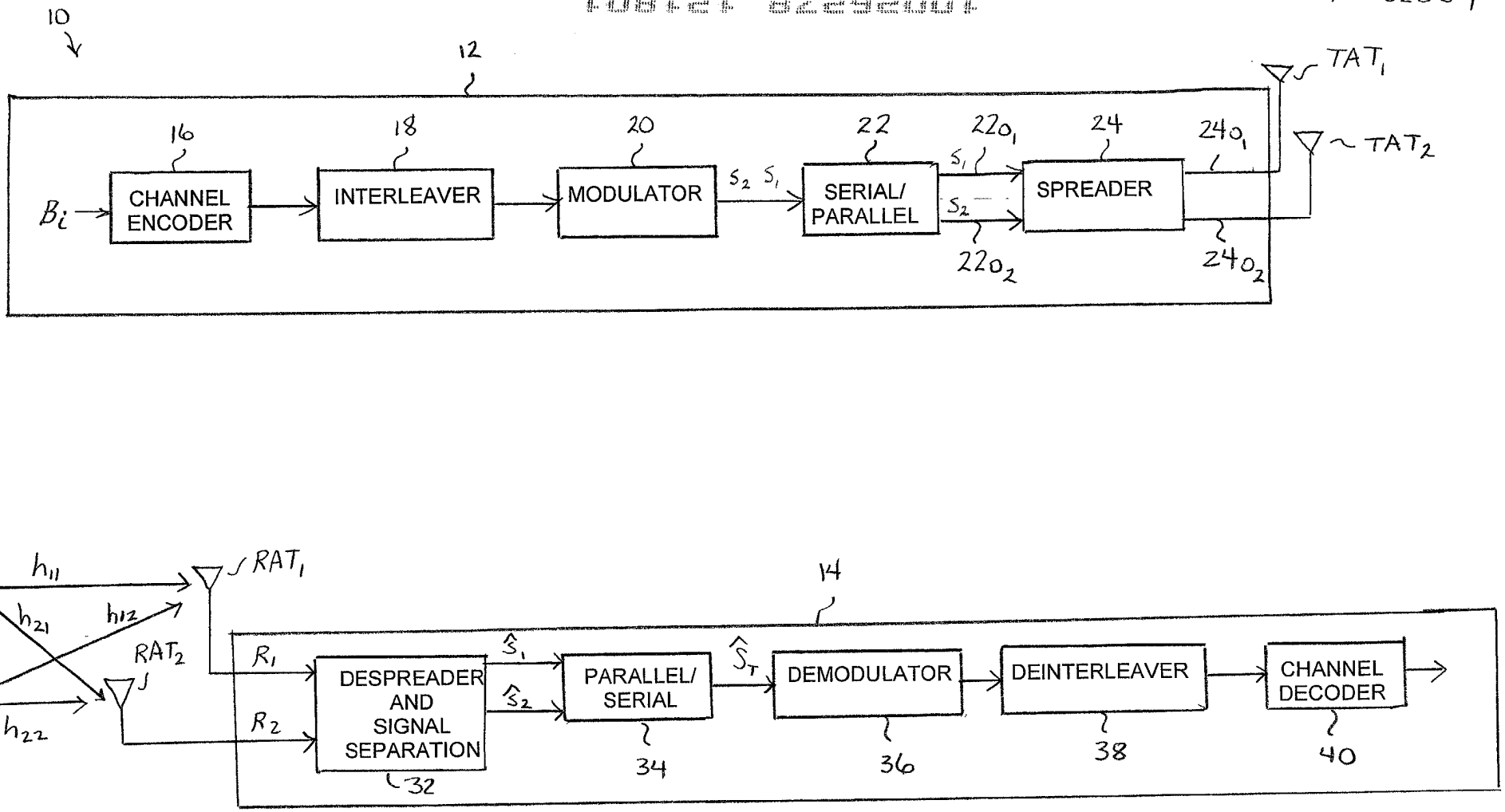
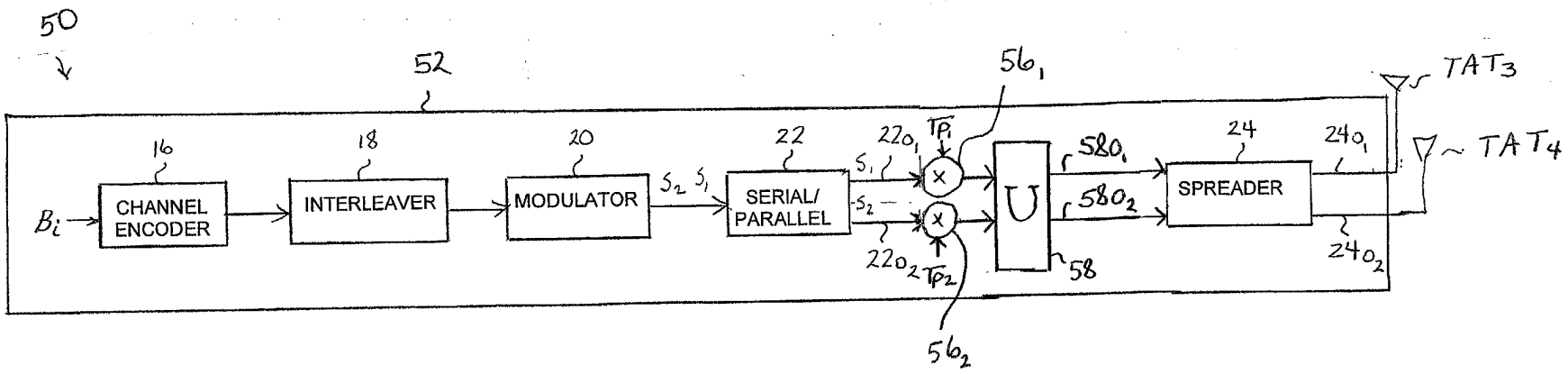


FIG. 1 (PRIOR ART)



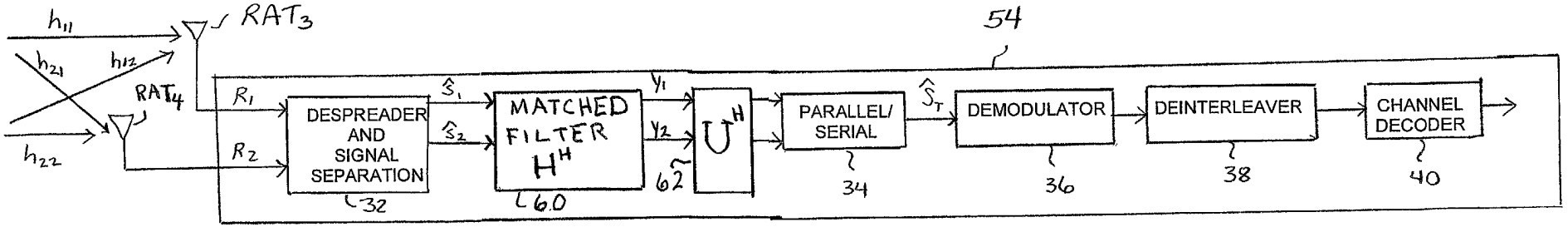


FIG. 2 (PRIOR ART)

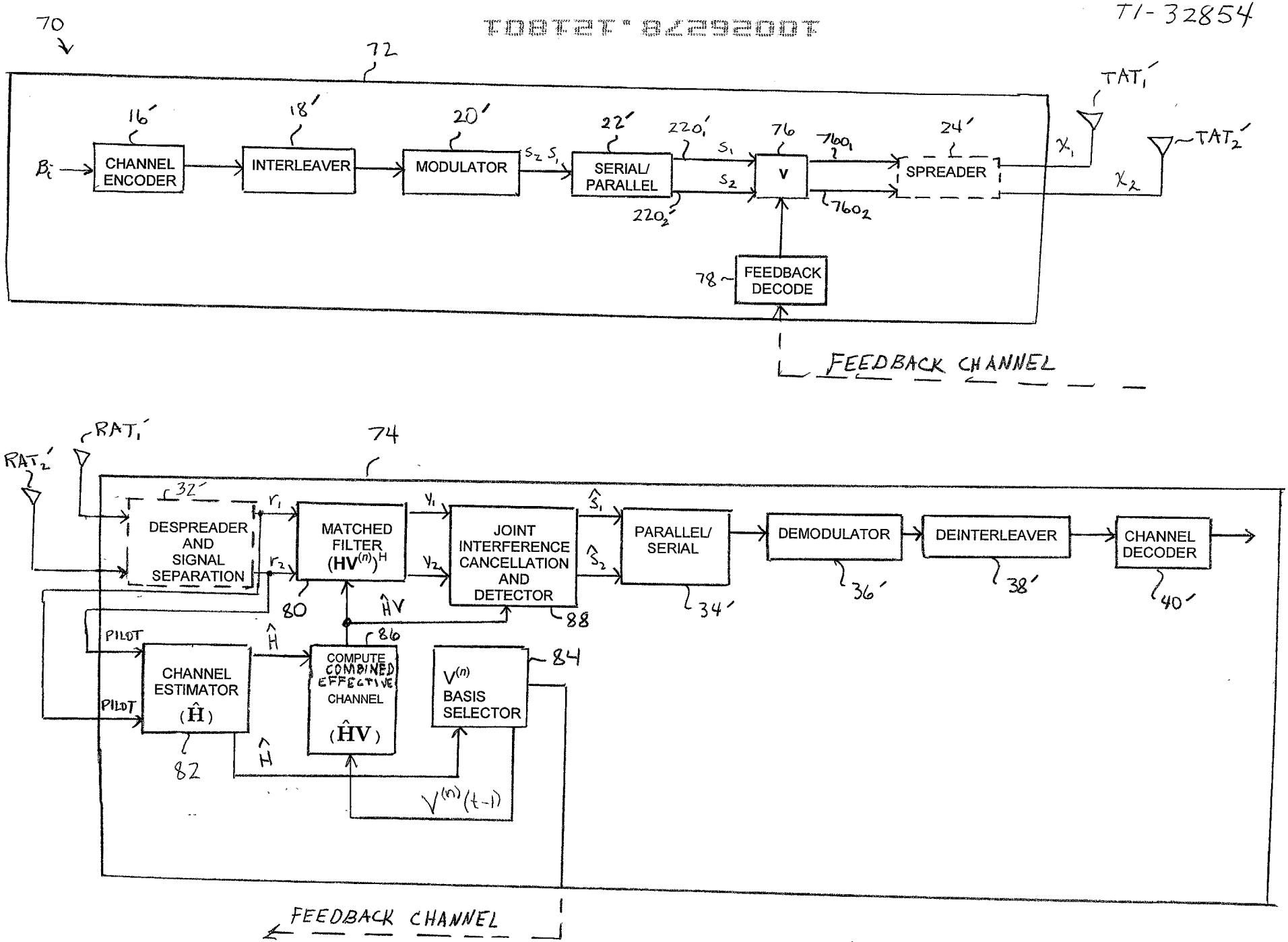

70
72
16'
18'
20'
22'
220₁'
220₂'
76
760₁
760₂
24'
B_i
CHANNEL ENCODER
INTERLEAVER
MODULATOR
S2 S1
SERIAL/ PARALLEL
S1
S2
V
SPREADER
TAT1'
TAT2'
X1
X2
78
FEEDBACK DECODE
FEEDBACK CHANNEL
RAT1'
RAT2'
74
32'
DESPREADER AND SIGNAL SEPARATION
r1
r2
MATCHED FILTER (HV(n))H
y1
y2
JOINT INTERFERENCE CANCELLATION AND DETECTOR
ŝ1
ŝ2
PARALLEL/ SERIAL
DEMODULATOR
DEINTERLEAVER
CHANNEL DECODER
34'
36'
38'
40'
80
ĤV
88
86
PILOT
PILOT
CHANNEL ESTIMATOR (Ĥ)
Ĥ
COMPUTE COMBINED EFFECTIVE CHANNEL (ĤV)
V(n) BASIS SELECTOR
84
82
V(n)(t-1)
FEEDBACK CHANNEL

FIG. 3

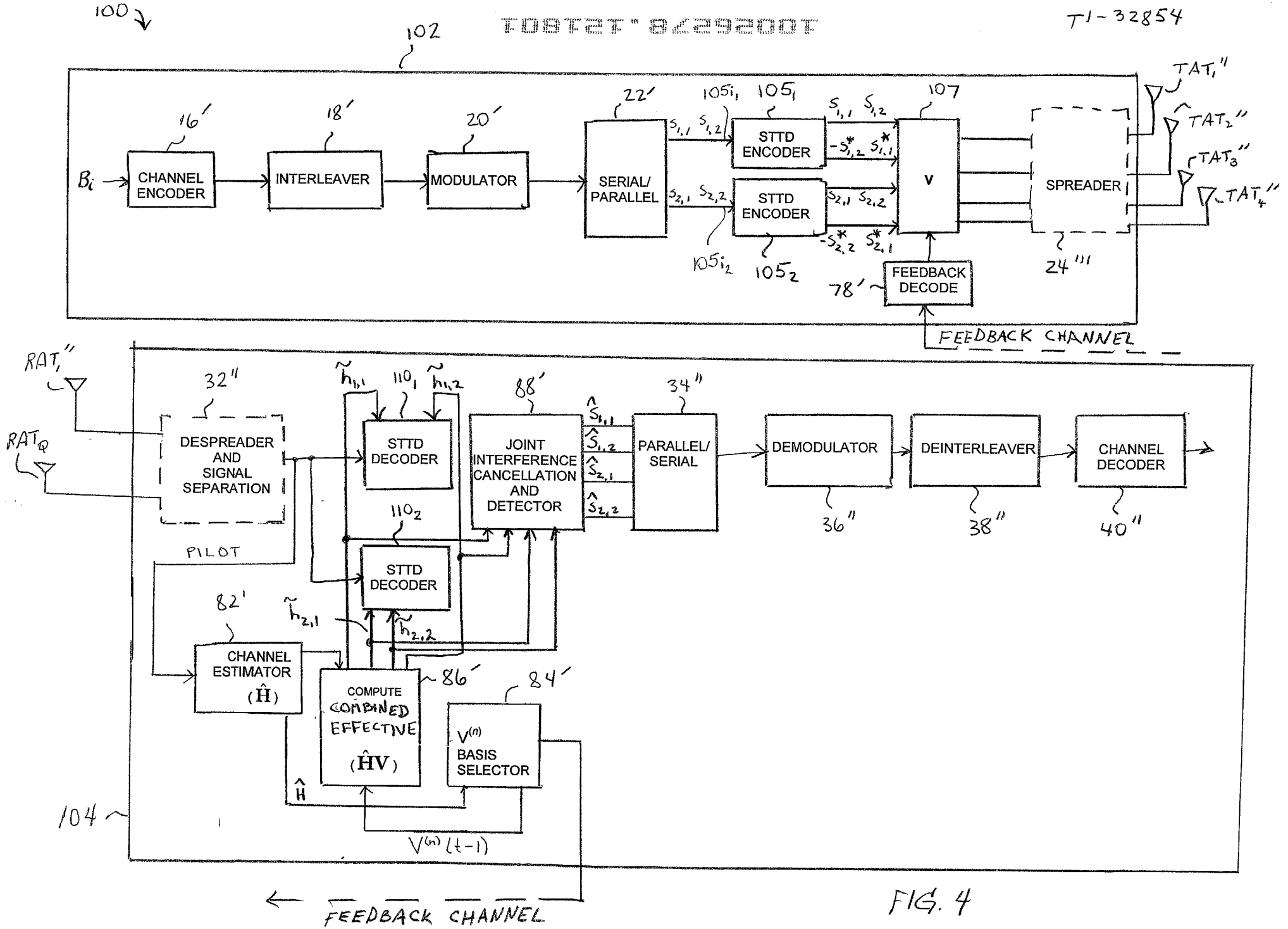

100
102
16'
18'
20'
22'
CHANNEL ENCODER
INTERLEAVER
MODULATOR
SERIAL/ PARALLEL
STTD ENCODER
STTD ENCODER
V
SPREADER
FEEDBACK DECODE
78'
24''
FEEDBACK CHANNEL
RAT1''
RATQ
32''
DESPREADER AND SIGNAL SEPARATION
STTD DECODER
STTD DECODER
JOINT INTERFERENCE CANCELLATION AND DETECTOR
88'
34''
PARALLEL/ SERIAL
DEMODULATOR
DEINTERLEAVER
CHANNEL DECODER
36''
38''
40''
PILOT
82'
CHANNEL ESTIMATOR (Ĥ)
COMPUTE COMBINED EFFECTIVE (ĤV)
86'
84'
V(n) BASIS SELECTOR
104
V(n)(t-1)
FEEDBACK CHANNEL

FIG. 4